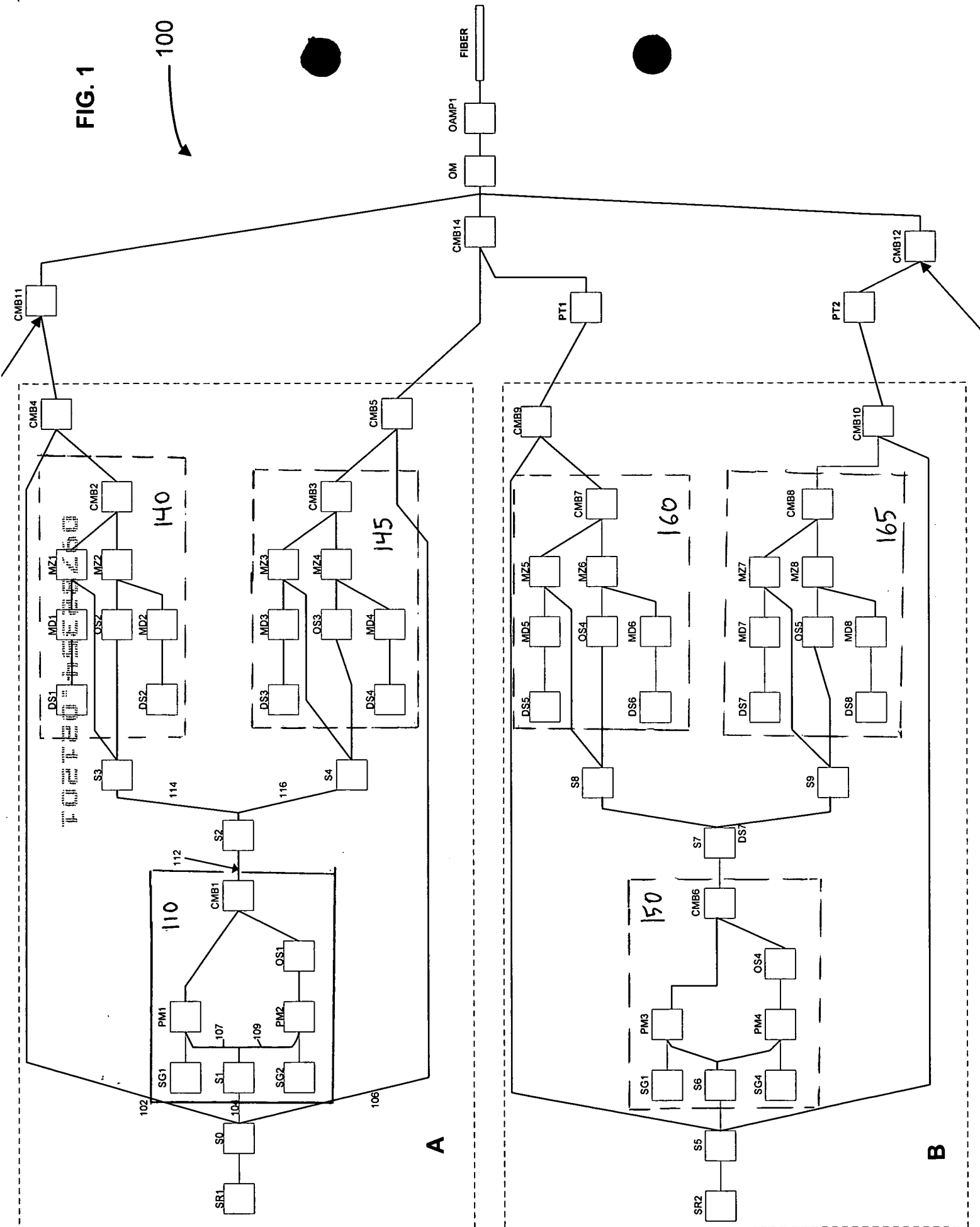
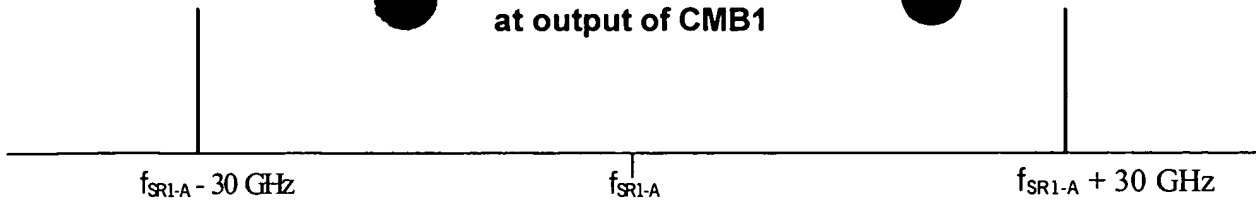


FIG. 1

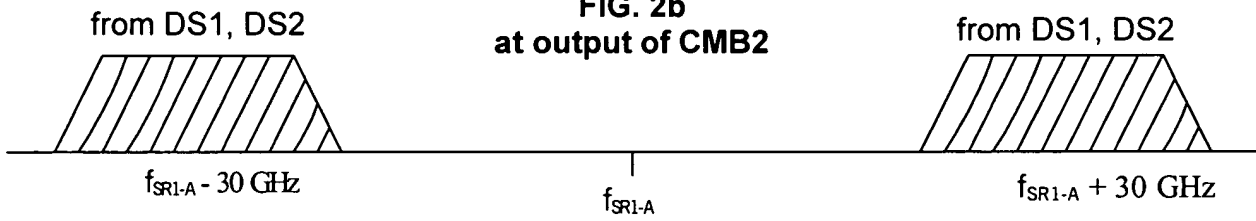
100



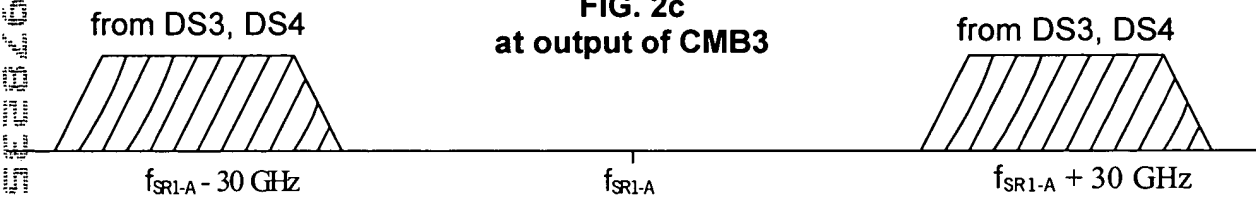
**FIG. 2a**  
at output of CMB1



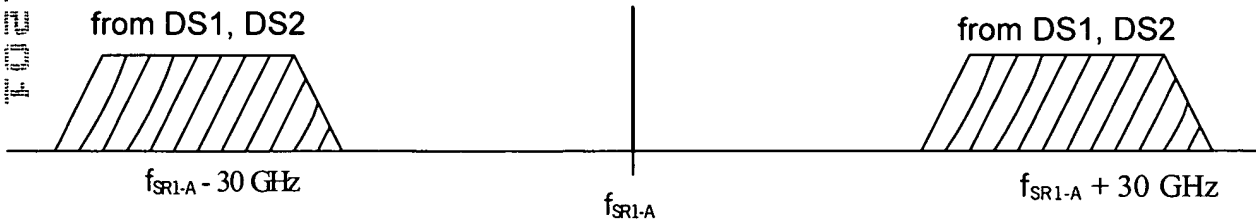
**FIG. 2b**  
at output of CMB2



**FIG. 2c**  
at output of CMB3



**FIG. 2d**  
at output of CMB4



**FIG. 2e**  
at output of CMB5

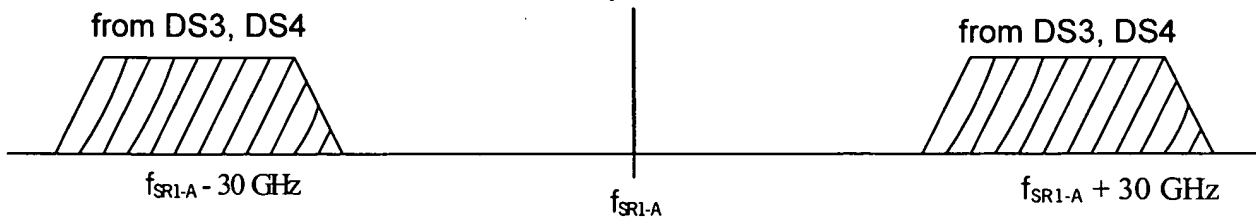


FIG. 2a-2e

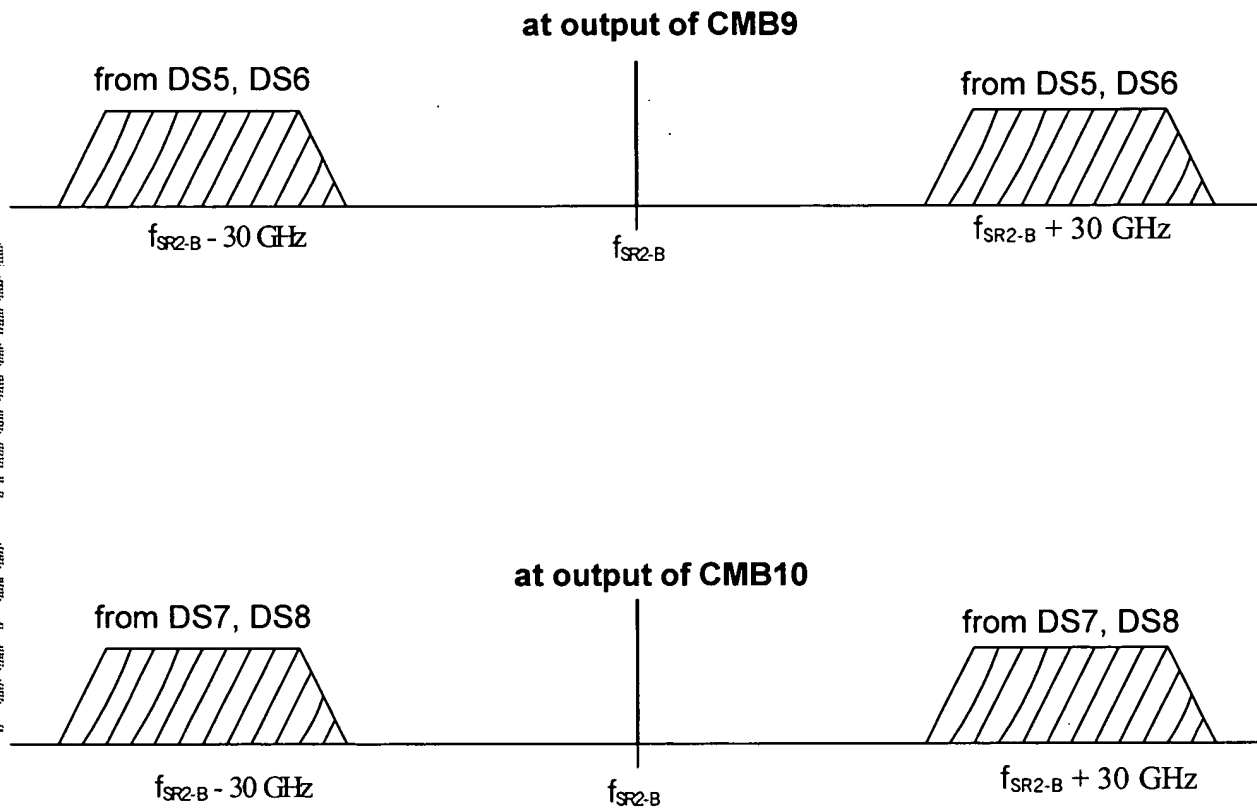
[illegible]

FIG. 4a

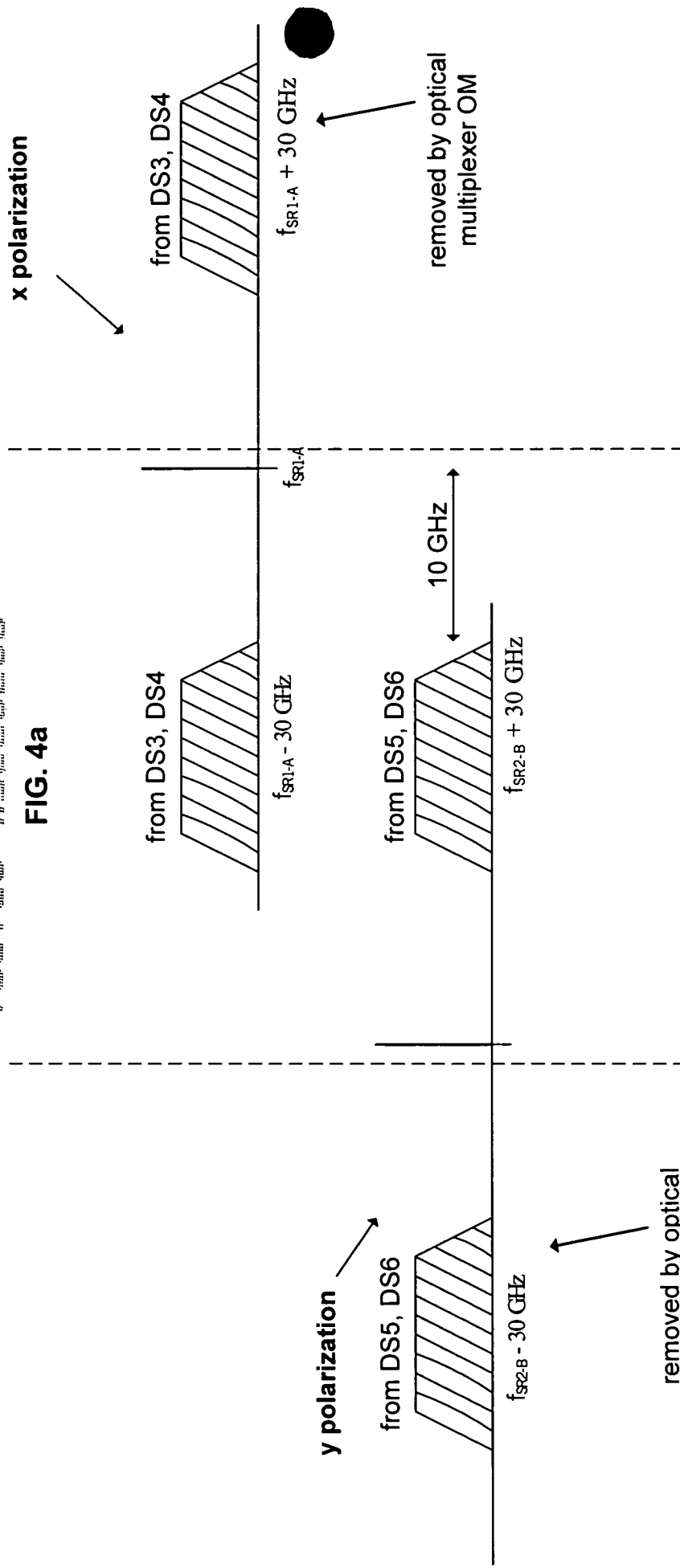
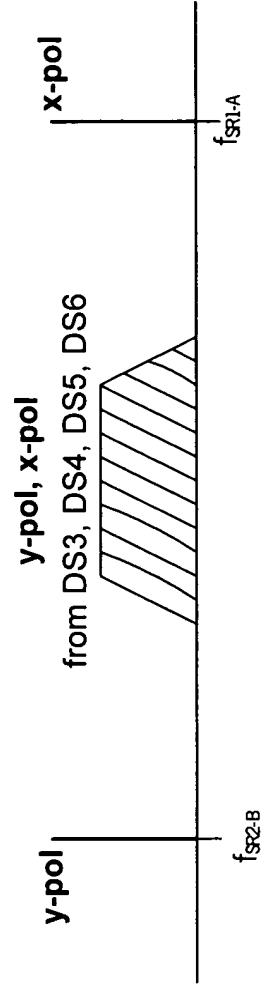


FIG. 4b





**FIG. 6**

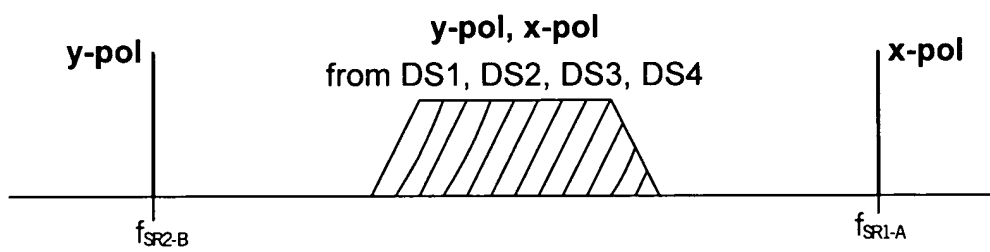


FIG. 6

FIG. 7a

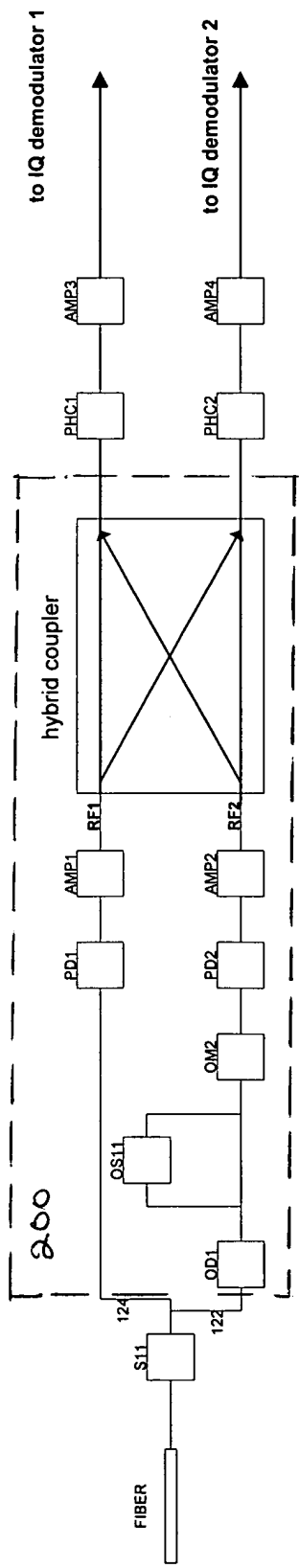


FIG. 7b

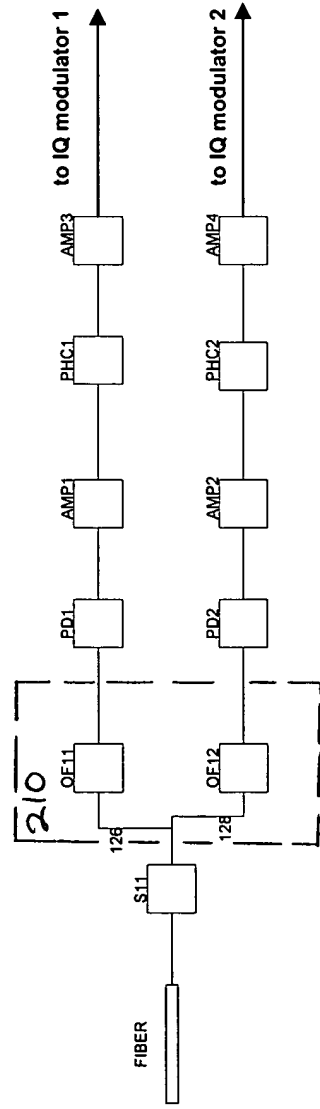
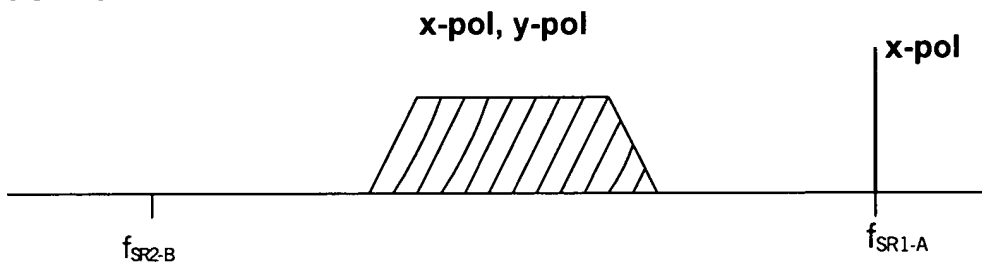


FIG. 8

AT OUTPUT OF OF11



AT OUTPUT OF OF12

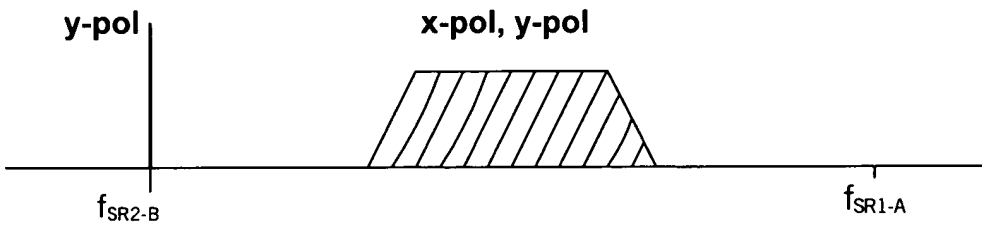


FIG. 8